at least one thin film dielectric (5) of a thickness in the range of about $0.25\text{-}0.75\mu\text{m}$ having a first surface disposed, on a second surface of the at least one first electrode opposing said first surface of the at least one first electrode, and

at least one second electrode (6) disposed on a second surface of the at least one dielectric opposing said first surface of the at least one dielectric,

wherein the at least one thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant $\epsilon_{\rm r}.$

9. (Thrice Amended) A voltage-controlled oscillator with

as its capacitive component a ceramic passive component

which comprises a carrier substrate (1), at least one first electrode (2) formed of a material selected from the group consisting of metals and alloys and having a first surface disposed on the substrate, at least one thin film dielectric (5) of a thickness in the range of about 0.25-



surface, opposed to said first surface of the at least first electrode, and at least a second electrode (6)

 $0.75\mu\mathrm{m}$ having a first surface disposed, on a second

disposed on a second surface of the at least one thin film

dielectric, opposed to said first surface of the at least

one dielectric, wherein the at least one thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant $\epsilon_{\rm r}$.

10. (Four Times Amended) A filter with as its capacitive component a ceramic passive component which comprises a carrier substrate (1), at least one first electrode (2) formed of a material selected from the group consisting of metals and alloys and having a first surface disposed on the substrate, at least one thin film dielectric (5) of a thickness in the range of about $0.25\text{-}0.75\mu\text{m}$ having a first surface disposed on a second surface of the at least one first electrode opposed to said first surface and at least one second electrode (6) having a surface disposed on said second surface of the at least one thin film dielectric wherein the at least one thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant ϵ_{r} .

11. (Thrice Amended) A delay line with as its capacitive component a ceramic passive component which comprises a carrier substrate (1), at least one first electrode formed of a material selected from the group consisting of metal and alloys and (2) having a first surface disposed on the



substrate at least one thin film dielectric (5) of a thickness in the range of about $0.25\text{-}0.75\mu\text{m}$ having a first surface disposed on a second surface of the one first electrode opposed to said first surface and at least one a second electrode (6) having a surface disposed on said second surface of the at least one thin film dielectric wherein the at least one thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant ϵ_{r} .



12. (Thrice Amended) A capacitive ceramic comprising a carrier substrate (1), at least one first electrode (2) formed of a material selected from the group consisting of metals and alloys and having a first surface disposed on the substrate at least one dielectric (5) of a thickness in the range of about 0.25-0.75 μm with a voltage-dependent relative dielectric constant ϵ_r having a second surface opposed to said first surface disposed on a second surface of the at least one first electrode opposed to said first surface and at least one second electrode (6) disposed on said second surface of the at least one thin film dielectric as a capacitive component.